

ABSTRACT OF THE DISCLOSURE

In an image reading device having a plurality of photoelectric conversion elements formed in one or more rows on an IC chip and a conductor layer having openings formed therein for limiting light striking the photoelectric conversion elements, a conductor having substantially the same width as the conductor layer is formed in the area extending from the photoelectric conversion element located at each end of the IC chip to the chip edge. This arrangement helps make light shield conditions nearly equal for the photoelectric conversion element located at an end of the IC chip and for the other photoelectric conversion elements.